



Attorney Docket No. 5649-1161

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hyun-Chul Kim

Application Serial No.: 10/663,967

Filed: September 16, 2003

For: **INTEGRATED CIRCUIT DEVICES HAVING FUSE STRUCTURES
INCLUDING BUFFER LAYERS AND METHODS OF FABRICATING THE SAME**

Date: October 9, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)**

Sir:

Attached is a form PTO-1449, together with a copy of each of the identified document(s). It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP.

This Information Disclosure Statement is submitted in accordance with 37 C.F.R. § 1.97(b), within three months of the filing date of the above-referenced application. Therefore, no fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

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Certificate of Mailing under 37 CFR § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 9, 2003.

Rosa Lee Brinson

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.